

**METHOD AND APPARATUS FOR PROTECTING AND TRANSMITTING THE
SIDE INFORMATION RELATED TO PEAK-TO-AVERAGE POWER RATIO
REDUCTION IN A MULTICARRIER SYSTEM**

FIELD OF THE INVENTION

5 **[0001]** The present invention generally relates to the field of peak-to-average power ratio (PAPR) reduction in multicarrier communication systems, such as discrete multitone (DMT) and orthogonal frequency division multiplexing (OFDM) communication systems, and more particularly to a method and apparatus for protection and transmission of the side information related to PAPR reduction in multicarrier
10 communication systems.

BACKGROUND OF THE INVENTION

[0002] Multicarrier communication systems, including the most commonly used DMT and OFDM communication systems, have attracted considerable attention in a variety of high-speed communication applications, including digital subscriber line
15 (DSL), digital terrestrial broadcasting, wireless local area network (WLAN), wireless metropolitan area network (WMAN), dedicated short range communication (DSRC), power line communication, and so on. They also show promise as future generation of mobile communication systems. The advantage of multicarrier communication systems comes from dividing high-speed data stream into multiple parallel portions of data
20 streams transmitted via individual sub-carriers. Each portion of data stream is transmitted at a lower speed and thus robust against the effects of channel impairments such as multipath fading and impulse noise.

[0003] FIG. 1 is a simplified block diagram illustrating a typical OFDM transmitter.

As can be seen from the OFDM transmitter, data $X[k]$, $k = 0, 1, \dots, N - 1$, to be transmitted within an OFDM symbol period, are transformed via a serial/parallel (S/P) converter 10, an N -point inverse fast Fourier transform (N -IFFT) 20, and a parallel/serial (P/S) converter 30 into the following baseband transmitted signal:

$$x[n] = \frac{1}{\sqrt{N}} \sum_{k=0}^{N-1} X[k] W_N^{kn}, \quad n = 0, 1, \dots, N - 1 \quad (1)$$

where

$$W_N \equiv e^{j2\pi/N} \quad (2)$$

is the twiddle factor. The discrete-time transmitted signal $x[n]$ given by (1) is then inserted with cyclic prefix followed by digital-to-analog (D/A) conversion, and the resultant analog signal $x(t)$ is sent to RF front end for further processing including in-phase/quadrature-phase (I/Q) modulation, up conversion and power amplification.

[0004] It is known that the PAPR of the analog signal $x(t)$ is higher than that of the discrete-time counterpart $x[n]$ by several dB, and can be approximated by that of $x[n/R]$ where $x[n/R]$ denotes the signal taken from R times oversampling of $x[n]$. Note that the cyclic prefix insertion 40 has no any effect on the PAPR level of $x(t)$. Thus, it is convenient to evaluate the PAPR level of $x(t)$ in terms of that of $x[n/R]$ given by

$$PAPR = \frac{\max_{0 \leq n \leq RN-1} |x[n/R]|^2}{E\{|x[n/R]|^2\}} \quad (3)$$

where $E\{ \}$ denotes expectation operation. Typically, the approximation is quite accurate

for $R \geq 4$.

[0005] One major drawback of multicarrier communication systems is the high PAPR of the baseband transmitted signal $x(t)$. When passing through an RF front end without sufficient power back-offs, the signal $x(t)$ will be distorted by the nonlinearity of RF power amplifier. In particular, the nonlinearity will incur not only the in-band signal distortion leading to bit-error-rate (BER) performance degradation, but also the out-of-band radiation (or spectrum re-growth) leading to adjacent channel interference and violation of government's spectrum regulation.

[0006] Conventional solution to this problem is simply utilizing a power amplifier with large linear range and large power back-offs at the expense of low power efficiency, high power consumption, and high manufacturing cost. Alternatively, the problem can be resolved by using PAPR reduction approaches. One of the PAPR reduction approaches is block-coding approach, which tries to find out a coding rule so that all the encoded codewords result in very low PAPR levels for the transmitted signal $x(t)$. However, extremely low code rate and extraordinary encoding/decoding complexity make the approach only suitable for systems with small constellation size and small number of sub-carriers.

[0007] Another PAPR reduction approach is deliberately clipping approach. In this approach, those amplitude levels of the transmitted signal exceeding a certain threshold are clipped, and the clipped signal is filtered to eliminate out-of-band radiation. Nevertheless, large clipping distortion may lead to severe BER performance degradation and inadequately filtering may lead to peak re-growth. On the other hand, probabilistic approach tries to reduce the probability of high PAPR level for the transmitted signal by changing the phase, order, level or other properties of the data stream. Probabilistic

approach includes partial transmit sequence (PTS) method, selective mapping (SLM) method, tone reservation (TR) method, tone injection (TI) method, and pulse superposition method, among which the PTS method seems to be most attractive in terms of implementation complexity as well as PAPR reduction performance.

5 **[0008]** For a number of PAPR reduction methods such as the PTS, SLM and pulse superposition methods, the associated receiver needs to know about the modifications (e.g., the modified phases, orders or levels) that have been made to the data stream at the transmitter during PAPR reduction procedure. The modifications are referred to as the side information, which is used for correctly recovering the original data stream at the
10 receiver. Correspondingly, the reliability of transmitting the side information related to PAPR reduction is extremely important for system's functionality.

[0009] FIG. 2 is the block diagram of an OFDM transmitter using the PTS method, which is disclosed in the US patent 6,125,103. For reducing the PAPR of $x(t)$, the input data block $\mathbf{X} = [X[0], X[1], \dots, X[N-1]]^T$ is first partitioned into L disjoint sub-blocks
15 (or clusters), denoted by $\mathbf{X}_1, \mathbf{X}_2, \dots, \mathbf{X}_L$, of length N where the superscript 'T' represents vector transposition. Only N/L entries of \mathbf{X}_l , $l \in \{1, 2, \dots, L\}$, are taken from the corresponding entries of \mathbf{X} and the remaining ones are set to zero. The partition scheme can be interleaved, adjacent, or irregular. The L disjoint sub-blocks are then phase-rotated and combined to form the following signal:

$$20 \quad \tilde{\mathbf{X}} = \sum_{l=1}^L b_l \mathbf{X}_l \quad (4)$$

or, equivalently,

$$\tilde{X}[k] = \sum_{l=1}^L b_l X_l[k], \quad k = 0, 1, \dots, N-1 \quad (5)$$

where b_l is the phase rotation factor (i.e., $|b_l| = 1$) associated with the l th sub-block X_l .

[0010] Taking N -IFFT of (5) yields the transmitted signal

$$\tilde{x}[n] = \sum_{l=1}^L b_l x_l[n], \quad n = 0, 1, \dots, N-1 \quad (6)$$

5 where $x_l[n]$, representing the N -IFFT of $X_l[k]$, is referred to as the PTS. The goal of the PTS method is to search for optimal phase sequence $\{b_1, b_2, \dots, b_L\}$ such that the PAPR level of the resultant transmitted signal is minimum. In practice, the phase of b_l is admitted to be one of a limited set of discrete values $\{2\pi m/M, m = 0, 1, \dots, M-1\}$, and b_1 can be fixed to unity without sacrificing any PAPR reduction performance. As such, finding optimal phase sequence $\{b_2, b_3, \dots, b_L\}$ requires performing $M^{(L-1)}$ computations of (3), implying that optimal search for $\{b_2, b_3, \dots, b_L\}$ is almost prohibitive for large L and M . For this reason, there have been low-complexity sub-optimal search algorithms, for which $\{b_2, b_3, \dots, b_L\}$ is selected from a smaller subset of all possible candidates of $\{b_2, b_3, \dots, b_L\}$. Obviously, sub-optimal search algorithms suffer from some degradation in PAPR reduction performance.

[0011] The phase sequence $\{b_2, b_3, \dots, b_L\}$ is considered as the side information to be transmitted to the associated receiver, so that the receiver can correctly recover the data stream of sub-blocks X_l , $l = 2, 3, \dots, L$. In conventional PTS method, the side information is transmitted via $(L-1)$ reserved sub-carriers where one sub-carrier within each sub-block X_l ($l \in \{2, 3, \dots, L\}$) is allocated. The conventional method, however, provides no

protection capability for the side information over these reserved sub-carriers and, thus, may result in unreliable side information detection at the receiver under noisy channel conditions. On the other hand, our invention provides a low-complexity and low-latency method and apparatus for reliably transmitting the side information regarding PAPR reduction in multicarrier communication systems.

SUMMARY OF THE INVENTION

[0012] The present invention has been made to overcome the above-mentioned drawback of conventional transmission of the side information related to PAPR reduction in multicarrier communication systems. An object of the present invention is to provide a low-complexity and low-latency method and apparatus, whereby the side information regarding PAPR reduction in the systems is reliably transmitted. In particular, the side information is encoded by an error-correction code and transmitted through a plurality of reserved sub-carriers. Modulation of the coded side information over the reserved sub-carriers is performed separately without re-performing data modulation. This invention thereby provides the advantages of dramatically reduced implementation complexity. Moreover, the proposed method is applicable to multicarrier communication systems with any number of sub-carriers and any type of data constellation, while introducing only slight or no degradation in PAPR reduction performance.

[0013] According to the invention, determination of the related parameters for PAPR reduction can be based on the PAPR of data modulated signal with slight degradation in PAPR reduction performance. Alternatively, it can also be based on the PAPR of transmitted signal with no loss of PAPR reduction performance.

[0014] The foregoing and other objects, features, aspects and advantages of the present invention will become better understood from a careful reading of the detailed description provided herein below with appropriate reference to the accompanying drawings.

5 **BRIEF DESCRIPTION OF THE DRAWINGS**

[0015] The present invention can be understood in more detail by reading the subsequent detailed description in conjunction with the examples and references made to the accompanying drawings, wherein:

FIG. 1 is a simplified block diagram illustrating a typical OFDM transmitter;

10 FIG. 2 is the block diagram of a conventional OFDM transmitter using the PTS method;

FIG. 3 is the block diagram of an OFDM transmitter illustrating the first embodiment of the present invention;

FIG. 4 is the block diagram of an OFDM transmitter illustrating another embodiment of the present invention;

15 FIG. 5 is a more detailed block diagram illustrating the OFDM modulator, the parameter control device for PAPR reduction, and the side information coding and modulation device shown in FIG. 3, in which the PTS method is used and K reserved sub-carriers are used for protecting and transmitting the side information;

FIG. 6 illustrates the allocation of the K reserved sub-carriers for the OFDM transmitter
20 shown in FIG. 5;

FIG. 7 is the block diagram of an OFDM receiver for detecting and decoding the

transmitted side information as well as for recovering the original data stream;

FIG. 8 are exemplary diagrams of computing the partial N -IFFT for (a) any given frequency p_1 and (b) a certain frequency $p_1 = N/8$, and (c) an exemplary diagram of constructing the look-up mapping table in a memory device to generate a period of the side information modulated signal $\tilde{x}_s[n]$ for $p_1 = N/8$;

FIG. 9 is a more detailed block diagram illustrating the OFDM modulator, the parameter control device for PAPR reduction, and the side information coding and modulation device shown in FIG. 4, in which the PTS method is used and K reserved sub-carriers are used for protecting and transmitting the side information;

FIG. 10 is a more detailed block diagram illustrating the OFDM modulator, the parameter control device for PAPR reduction, and the side information coding and modulation device shown in FIG. 3, in which the PTS method is used and two groups of reserved sub-carriers are used for protecting and transmitting the side information;

FIG. 11 illustrates the allocation of the two groups of reserved sub-carriers for the OFDM transmitter in FIG. 10;

FIG. 12 is a more detailed block diagram illustrating the OFDM modulator, the parameter control device for PAPR reduction, and the side information coding and modulation device shown in FIG. 4, in which the PTS method is used and two groups of reserved sub-carriers are used for protecting and transmitting the side information;

FIGs. 13(a) and 13(b) plot the complementary cumulative distribution function of the obtained 10^5 independent realizations of PAPR levels for 128 and 1024 sub-carriers, respectively; and

FIGs. 14(a) and 14(b) plot the word-error-rate performance of the 'word' $\{b_2, b_3, b_4\}$ for the frequency flat and frequency selective channels, respectively.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0016] FIG. 3 is the block diagram of an OFDM transmitter in accordance with the first embodiment of the present invention. Referring to FIG. 3, the data $X[k]$, $k = 0, 1, \dots, N - 1$, to be transmitted within an OFDM symbol period, are transformed via a S/P converter 10 into the input data block \mathbf{X} . The OFDM modulator 100 having a parameter control device 200 for PAPR reduction performs OFDM modulation of the input data \mathbf{X} and generates the data modulated signal $\tilde{\mathbf{x}}_D$. The parameter control device 200 sequentially selects a set of parameters from all possible sets of parameters for PAPR reduction, and sends each set of selected PAPR reduction parameters to the OFDM modulator 100. The OFDM modulator 100 further modulates the input data \mathbf{X} in accordance with the PAPR reduction parameters and derives the corresponding data modulated signal $\tilde{\mathbf{x}}_D$. The procedure of selecting a new set of PAPR reduction parameters and deriving the corresponding data modulated signal $\tilde{\mathbf{x}}_D$ is repeated until an acceptable PAPR level of $\tilde{\mathbf{x}}_D$ is achieved. Alternately, it is repeated until all possible sets of PAPR reduction parameters have been selected and all the PAPR levels of the corresponding $\tilde{\mathbf{x}}_D$ have been computed. Then, a set of optimal (or sub-optimal) PAPR reduction parameters is determined according to the computed PAPR levels of $\tilde{\mathbf{x}}_D$.

[0017] After the set of optimal (or sub-optimal) PAPR reduction parameters has been determined, the side information coding and modulation device 300 performs encoding and OFDM modulation for the related information of the optimal (or sub-optimal) PAPR

reduction parameters, referred to as the side information, and derives the side information modulated signal \tilde{x}_s . The side information modulated signal \tilde{x}_s and the data modulated signal \tilde{x}_d are further combined via an adder 400 to yield the transmitted signal \tilde{x} , which is then transformed into the transmitted sequence $\tilde{x}[n]$ via a P/S converter 30. Note that

5 adding the side information modulated signal \tilde{x}_s to the data modulated signal \tilde{x}_d may lead to the resultant transmitted signal \tilde{x} with a slightly higher PAPR level than that of \tilde{x}_d . Because the PAPR reduction parameters are determined based on the data modulated signal \tilde{x}_d , a slight degradation in PAPR reduction performance could be introduced.

[0018] In the first embodiment of FIG. 3, several sub-carriers are reserved for

10 transmitting the side information, while the rest of sub-carriers are used for data transmission. The side information according to the embodiment of the invention can thus be properly protected. Moreover, modulation of the coded side information over the reserved sub-carriers and modulation of the data over the rest of sub-carriers are performed separately, thereby providing the benefit of dramatically reduced

15 implementation complexity.

[0019] FIG. 4 is the block diagram of an OFDM transmitter in accordance with another embodiment of the present invention. The data $X[k]$, $k = 0, 1, \dots, N - 1$, to be transmitted within an OFDM symbol period, are transformed via a S/P converter 10 into the input data block X . The OFDM modulator 100 having a parameter control device 200

20 for PAPR reduction performs OFDM modulation of the input data X and generates the data modulated signal \tilde{x}_d . Similar to the embodiment of FIG. 3, several sub-carriers are reserved for transmitting the side information. In the embodiment of FIG. 4, how to yield

the transmitted sequence $\tilde{x}[n]$ will become better understood from the detailed description provided herein below with reference to FIG. 4.

[0020] Referring to FIG. 4, during the procedure of determining optimal (or sub-optimal) PAPR reduction parameters, the parameter control device 200 sends each set of selected PAPR reduction parameters to the OFDM modulator 100. The OFDM modulator 100 modulates the input data \mathbf{X} in accordance with these PAPR reduction parameters and derives the corresponding data modulated signal \tilde{x}_D . At the same time, the side information coding and modulation device 300 performs encoding and OFDM modulation for the related information of the selected PAPR reduction parameters, referred to as the side information, and derives the side information modulated signal \tilde{x}_S . The side information modulated signal \tilde{x}_S and the data modulated signal \tilde{x}_D are further combined via an adder 400 to yield the transmitted signal \tilde{x} . The procedure of selecting a new set of PAPR reduction parameters and deriving the corresponding transmitted signal \tilde{x} is repeated until an acceptable PAPR level of \tilde{x} is achieved. Alternatively, it is repeated until all possible sets of PAPR reduction parameters have been selected and all the PAPR levels of the corresponding \tilde{x} have been computed. Then, a set of optimal (or sub-optimal) PAPR reduction parameters is determined according to the computed PAPR levels of \tilde{x} . After the PAPR reduction procedure has been completed, the finally resultant transmitted signal \tilde{x} is transformed into the transmitted sequence $\tilde{x}[n]$ via a P/S converter 30.

[0021] The thereby obtained optimal (or sub-optimal) PAPR reduction parameters always reflect the PAPR level of the finally resultant transmitted sequence $\tilde{x}[n]$. Thus,

the embodiment of FIG. 4 can protect the side information without any degradation of PAPR reduction performance, while introducing only slightly increased computation complexity.

[0022] In the following, for clear and simple presentation, the present invention is further illuminated in terms of only the PTS method, although it can also be applied to other types of PAPR reduction methods.

[0023] FIG. 5 is a more detailed block diagram illustrating the OFDM modulator 100, the parameter control device 200 for PAPR reduction, and the side information coding and modulation device 300 shown in FIG. 3. In FIG. 5, the parameter control device 200 determines an optimal (or sub-optimal) phase sequence $\{b_2, b_3, \dots, b_L\}$ according to the PAPR level of the data modulated signal \tilde{x}_D from the OFDM modulator 100. As mentioned before, when the PTS method searches for optimal (or sub-optimal) phase sequence $\{b_1, b_2, \dots, b_L\}$, the phase of b_l is admitted to be one of a limited set of discrete values $\{2\pi m/M, m = 0, 1, \dots, M - 1\}$, and b_1 can be fixed to unity without sacrificing any PAPR reduction performance.

[0024] In the following, how to protect and transmit the side information about the phase sequence $\{b_2, b_3, \dots, b_L\}$ via K reserved sub-carriers is further described.

[0025] In order to easily encode the phase sequence $\{b_2, b_3, \dots, b_L\}$, the present invention first establishes a one-by-one mapping between each of the possibly used $\{b_2, b_3, \dots, b_L\}$ and a corresponding index i , where $i \in \{0, 1, \dots, I - 1\}$ and I denotes the number of the possible candidates of $\{b_2, b_3, \dots, b_L\}$. Note that $I = M^{(L-1)}$ for optimal search algorithms and may be less than $M^{(L-1)}$ for suboptimal search algorithms. Table

1 shows an example of such mapping with $L = 4$ (four sub-blocks) and $M = 2$ ($b_l \in \{\pm 1\}$) for optimal search algorithms ($I = 2^3 = 8$).

Table 1. An exemplary mapping table for optimal search algorithms

($L = 4$, $M = 2$ and $I = 8$)

Index		$b_2 \ b_3 \ b_4$
i	Binary representation	
0	000	1 1 1
1	001	1 1 -1
2	010	1 -1 1
3	011	1 -1 -1
4	100	-1 1 1
5	101	-1 1 -1
6	110	-1 -1 1
7	111	-1 -1 -1

[0026] In FIG. 5, the parameter control device 200 for PAPR reduction further comprises a phase optimization unit 60 and a phase mapper 70. The phase optimization unit 60 sequentially selects an index i from the set of $\{1, 2, \dots, I\}$ and the phase mapper 70 generates the associated phase sequence $\{b_2, b_3, \dots, b_L\}$ according to the mapping as illustrated in Table 1, so that the OFDM modulator 100 can derive a new data modulated

signal \tilde{x}_D . Such step is repeated until an optimal (or sub-optimal) phase sequence $\{b_2, b_3, \dots, b_L\}$ is found.

[0027] Let i_s denote the index i corresponding to the determined $\{b_2, b_3, \dots, b_L\}$ after phase optimization, then knowing the index i_s is equivalent to knowing the determined
5 sequence $\{b_2, b_3, \dots, b_L\}$. Thus, the side information about $\{b_2, b_3, \dots, b_L\}$ can be simply protected by error-correction encoding of the index i_s .

[0028] Further referring to FIG. 5, after the phase optimization unit 60 has determined the optimal (or sub-optimal) index i_s , the index i_s is fed to the side information coding and modulation device 300 to generate the side information
10 modulated signal \tilde{x}_s . The side information coding and modulation device 300 uses an error-correction code to protect the side information (the index i_s), and performs OFDM modulation of the coded side information. Specifically, the side information coding and modulation device 300 comprises an encoder 310 to proceed error-correction encoding of the index i_s , and the resultant codeword is mapped into a sequence $\{d_1, d_2, \dots, d_K\}$ of K
15 symbols via a symbol mapper 320. The sequence $\{d_1, d_2, \dots, d_K\}$ is then modulated onto K reserved sub-carriers $\{p_1, p_2, \dots, p_K\}$ by means of N -IFFT.

[0029] In general, the number of reserved sub-carriers is very few in the embodiment of FIG. 5. This allows the embodiment of FIG. 5 to simplify the required operations for N -IFFT as a partial N -IFFT 330 with different degree of complexity according to
20 different arrangement of the reserved sub-carriers $\{p_1, p_2, \dots, p_K\}$. The simplified process will be described later.

[0030] FIG. 6 illustrates the allocation of the K reserved sub-carriers for the OFDM

transmitter in FIG. 5, in which the reserved sub-carriers are used for transmitting the coded side information. The data modulated signal \tilde{x}_D and the side information modulated signal \tilde{x}_S are corresponding to the N -IFFTs of the following signals, respectively:

$$\tilde{X}_D[k] = \sum_{l=1}^L b_l X_l[k], \quad k = 0, 1, \dots, N-1, \quad (7)$$

and

$$\tilde{X}_S[k] = \begin{cases} d_m, & \text{for } k = p_m, \quad m = 1, 2, \dots, K \\ 0, & \text{otherwise} \end{cases} \quad (8)$$

where

$$X_l[k] = 0, \quad \text{for } k = p_m, \quad m = 1, 2, \dots, K \quad (9)$$

are reserved for transmitting the coded side information. The adder 400 in FIG. 5 sums up \tilde{x}_D and \tilde{x}_S , and generates the transmitted sequence

$$\tilde{x}[n] = \tilde{x}_D[n] + \tilde{x}_S[n], \quad n = 0, 1, \dots, N-1. \quad (10)$$

[0031] Further referring to both FIG. 5 and FIG. 6 and according to (7) to (9), taking N -IFFT of (10) yields

$$\tilde{X}[k] = \tilde{X}_D[k] + \tilde{X}_S[k] = \begin{cases} d_m, & \text{if } k = p_m, \quad m = 1, 2, \dots, K \\ \tilde{X}_D[k], & \text{otherwise} \end{cases} \quad (11)$$

Accordingly, an OFDM receiver for recovering both the side information and the original data is illustrated in FIG. 7. Referring to FIG. 7, the OFDM receiver receives the sequence $y[n]$, which is transformed into the data block y via a S/P converter 10. After

the received data y have been proceeded through an N -point fast Fourier transform (N -FFT) 550 and an equalizer 540, K symbols on the reserved sub-carriers $\{p_1, p_2, \dots, p_K\}$ are picked out. Then, from the K symbols, the side information demodulation and decoding device 500 finds out the phase sequence $\{\hat{b}_2^*, \hat{b}_3^*, \dots, \hat{b}_L^*\}$ that has been
5 determined at the transmitter. According to the obtained phase sequence $\{\hat{b}_2^*, \hat{b}_3^*, \dots, \hat{b}_L^*\}$, the OFDM receiver removes the phase rotations made at the transmitter and recovers the data sequence $\hat{X}[k]$.

[0032] As can be seen from FIG. 7, the side information demodulation and decoding device 500 further comprises a symbol de-mapper 510, a decoder 520 and a phase
10 mapper 530 for finding out the phase sequence determined at the transmitter. The symbol de-mapper 510 de-maps the K symbols on the sub-carriers $\{p_1, p_2, \dots, p_K\}$ into a corresponding codeword, and the decoder 520 decodes this codeword into a corresponding index \hat{i}_s . The phase mapper 530 then transforms the index \hat{i}_s into a corresponding phase sequence $\{\hat{b}_2^*, \hat{b}_3^*, \dots, \hat{b}_L^*\}$.

15 [0033] Further referring to FIG. 5, the complexity for deriving the side information modulated signal \tilde{x}_s or, equivalently, for computing the N -IFFT of $\tilde{X}_s[k]$ can be dramatically reduced by virtue of the periodic property of the twiddle factor W_N as well as the fact that the number of reserved sub-carriers is very few in general. This is explained by considering the following case. The input data block \mathbf{X} is divided into four
20 sub-blocks ($L = 4$), and the coded side information is mapped into the sequence $\{d_1, d_2, d_3, d_4\}$ ($K = 4$) which is to be transmitted through sub-carriers $\{p_1, p_2, p_3, p_4\} = \{p_1, p_1$

+ $N/4, p_1 + 2N/4, p_1 + 3N/4$ }.

[0034] Then, it can be easily shown that the side information modulated signal

$$\begin{aligned}\tilde{x}_s[n] &= \frac{1}{\sqrt{N}} \sum_{k=0}^{N-1} \tilde{X}_s[k] W_N^{kn} \\ &= W_N^{np_1} \cdot \frac{1}{\sqrt{N}} \sum_{m=0}^3 d_{m+1} W_4^{mn}, \quad n = 0, 1, \dots, N-1\end{aligned}\tag{12}$$

This reveals that the N -IFFT of $\tilde{X}_s[k]$ can be simply implemented by a 4-IFFT

5 followed by $(N - 1)$ complex multipliers (associated with $W_N^{np_1}$), as shown in FIG. 8(a).

[0035] When $p_1 = N/8$, the implementation in FIG. 8(a) reduces to the one in FIG.

8(b) where only a 4-IFFT and four complex multipliers are needed. Furthermore, only a

4-IFFT is needed for $p_1 = 0$. Note that the original N -IFFT requires $N \log_2 N$ complex additions and $(N/2) \log_2 N$ complex multiplication operations, whereas the simplified

10 implementation, referred to as the partial N -IFFT 330 for clarity, requires only eight complex additions and $(N + 3)$ complex multiplication operations for any given

frequency p_1 . On the other hand, generation of the side information modulated signal

$\tilde{x}_s[n]$ depends only on the given index i , implying that $\tilde{x}_s[n]$ can be generated by simply looking through a mapping table that has been constructed in advance for all $i \in$

15 $\{0, 1, \dots, I - 1\}$ and can be stored in a memory device such as a read-only memory

(ROM). Similarly, the size of the mapping table (the memory device) can be reduced

dramatically by virtue of the above-mentioned fact and the periodic property of the twiddle factor, as exemplified in FIG. 8(c) for $p_1 = N/8$.

[0036] Note that in the embodiment of FIG. 5, determination of the index i_s is based

on the PAPR level of the data modulated signal \tilde{x}_D , implying that adding the side information modulated signal \tilde{x}_S to the signal \tilde{x}_D as given by (10) may lead to an increased PAPR level of the transmitted signal \tilde{x} . The resultant degradation of PAPR reduction performance, however, can be negligible if the total power of \tilde{x}_S is much less than that of \tilde{x}_D .

[0037] Alternatively, another embodiment as shown in FIG. 9 of the present invention determines the index i_S based on the PAPR level of the transmitted signal \tilde{x} without any degradation of PAPR reduction performance. FIG. 9 is a more detailed block diagram of the OFDM transmitter shown in FIG. 4, in which the parameter control device 200 for PAPR reduction and the side information coding and modulation device 300 are further illustrated. The side information coding and modulation device 300 generates the side information modulated signal \tilde{x}_S for every selected index $i \in \{0, 1, \dots, I-1\}$ during phase optimization, and thus the embodiment of FIG. 9 requires more computations than that of FIG. 5.

[0038] Next, other embodiments of the present invention with further reduced complexity are provided. First, the invention divides the sequence $\{d_1, d_2, \dots, d_K\}$ regarding the coded side information into two sequences, $\{d_1, d_2, \dots, d_{\tilde{K}}\}$ and $\{\sqrt{P_b}b_2, \sqrt{P_b}b_3, \dots, \sqrt{P_b}b_L\}$, where $\tilde{K} + (L-1) = K$ and P_b is the average power for transmitting the latter sequence. The former sequence $\{d_1, d_2, \dots, d_{\tilde{K}}\}$ is to be modulated onto \tilde{K} reserved sub-carriers $\{p_1, p_2, \dots, p_{\tilde{K}}\}$ for transmitting part of the coded side information, while the latter sequence $\{\sqrt{P_b}b_2, \sqrt{P_b}b_3, \dots, \sqrt{P_b}b_L\}$ is to be

imposed onto $(L - 1)$ reserved sub-carriers $\{q_2, q_3, \dots, q_L\}$ for transmitting the remaining part of the coded side information.

[0039] FIG. 10 is a more detailed block diagram illustrating the OFDM transmitter shown in FIG. 3, in which the PTS method is used and the parameter control device 200 for PAPR reduction and the side information coding and modulation device 300 are further illustrated. Referring to FIG. 10, the parameter control device 200 for PAPR reduction comprises a phase optimization unit 60 and a phase mapper 70. The phase mapper 70 is implemented by an encoder 72 followed by an M -ary phase-shift keying (PSK) mapper 71. For optimal search algorithms, the encoder 72 is inexistent and the phase mapper 70 reduces to the M -ary PSK mapper 71, which is illustrated again as in Table 1 with binary bits $\{0, 1\}$ mapped into BPSK symbols $\{+1, -1\}$, respectively. The BPSK symbols from the M -ary PSK mapper 71 ($M = 2$) are then used as the phase rotation factors $b_l, l = 2, 3, \dots, L$.

[0040] For sub-optimal algorithms, Table 2 shows another example of such phase mapping with $L = 4$ (four sub-blocks) and $M = 4$ ($b_l \in \{\pm 1, \pm j\}$), where only $I (= 8)$ candidates of $\{b_2, b_3, b_4\}$ out of total $64 (= 4^3)$ candidates are used for selecting sub-optimal phase sequence $\{b_2, b_3, b_4\}$. The index i in Table 2 is encoded by the encoder 72 with a linear $(6, 3)$ block code of generator matrix

$$\mathbf{G} = \begin{bmatrix} 0 & 1 & 1 & 1 & 0 & 0 \\ 1 & 0 & 1 & 0 & 1 & 0 \\ 1 & 1 & 0 & 0 & 0 & 1 \end{bmatrix} \quad (13)$$

and two-tuple binary bits $\{00, 01, 11, 10\}$ in the resultant codeword are mapped into quaternary phase shift keying (QPSK) symbols $\{1, j, -1, -j\}$, respectively. The QPSK

symbols from the M -ary PSK mapper 71 ($M = 4$) are then used as the phase rotation factors $b_l, l = 2, 3, \dots, L$.

Table 2. An exemplary mapping table for sub-optimal search algorithms

($L = 4, M = 4$ and $I = 8$)

Index		codeword			b_2 b_3 b_4		
i	Binary representation						
0	000	00	00	00	1	1	1
1	001	11	00	01	-1	1	j
2	010	10	10	10	$-j$	$-j$	$-j$
3	011	01	10	11	j	$-j$	-1
4	100	01	11	00	j	-1	1
5	101	10	11	01	$-j$	-1	j
6	110	11	01	10	-1	j	$-j$
7	111	00	01	11	1	j	-1

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[0041] As shown in FIG. 10, two-stage encoding of the index i_s is provided to protect the side information. During phase optimization, the first-stage encoding of each index i is performed by the encoder 72 and the resultant codeword is mapped to the sequence $\{b_2,$

$b_3, \dots, b_L\}$ by the M -ary PSK mapper 71. By allocating one sub-carrier per sub-block with the value of $\sqrt{P_b}$, the sequence $\{\sqrt{P_b}b_2, \sqrt{P_b}b_3, \dots, \sqrt{P_b}b_L\}$ for index i can be imposed onto the $(L - 1)$ reserved sub-carriers $\{q_2, q_3, \dots, q_L\}$, where sub-carrier q_l is within the frequency range of the l th sub-block. After phase optimization, the codeword for the index i_s from the encoder 72 is fed to the side information coding and modulation device 300 for second-stage encoding and modulation. The side information coding and modulation device 300 comprises a parity-bit generator 315, a symbol mapper 320 for mapping the generated parity bits into the sequence $\{d_1, d_2, \dots, d_{\tilde{K}}\}$, and a partial N -IFFT 330 for performing OFDM modulation of the sequence $\{d_1, d_2, \dots, d_{\tilde{K}}\}$ according to the arrangement of sub-carriers $\{p_1, p_2, \dots, p_{\tilde{K}}\}$.

[0042] FIG. 11 shows an example illustrating the allocation of the two groups of reserved sub-carriers $\{p_1, p_2, \dots, p_{\tilde{K}}\}$ and $\{q_2, q_3, \dots, q_L\}$ for two-stage protection of the side information. Let the N -FFT of the data modulated signal $\tilde{x}_D[n]$ be given as (7) with

$$X_l[k] = \begin{cases} 0, & \text{for } k = p_m, m = 1, 2, \dots, \tilde{K} \\ \sqrt{P_b}, & \text{for } k = q_l, l = 2, 3, \dots, L \end{cases} \quad (14)$$

and the N -FFT of the side information modulated signal $\tilde{x}_S[n]$ be given as

$$\tilde{X}_s[k] = \begin{cases} d_m, & \text{for } k = p_m, m = 1, 2, \dots, \tilde{K} \\ 0, & \text{otherwise} \end{cases} \quad (15)$$

It can be easily seen, from (7), (10), (14) and (15), that

$$\tilde{X}[k] = \tilde{X}_D[k] + \tilde{X}_S[k] = \begin{cases} d_m, & \text{for } k = p_m, m = 1, 2, \dots, \tilde{K} \\ \sqrt{P_b} \cdot b_l, & \text{for } k = q_l, l = 2, 3, \dots, L \\ \tilde{X}_D[k], & \text{otherwise} \end{cases} \quad (16)$$

This reveals that with the allocation as in FIG. 11, the two sequences $\{d_1, d_2, \dots, d_{\tilde{K}}\}$ and $\{\sqrt{P_b}b_2, \sqrt{P_b}b_3, \dots, \sqrt{P_b}b_L\}$ representing the two-stage coded side information are put on two groups of reserved sub-carriers $\{p_1, p_2, \dots, p_{\tilde{K}}\}$ and $\{q_2, q_3, \dots, q_L\}$, respectively. Therefore, the resultant transmitted signal $\tilde{x}[n]$ contains both groups of symbols for transmission of the index i_s . The corresponding receiver is similar to that in FIG. 7.

[0043] Because of the fact that $\tilde{K} < K$, the embodiment of FIG. 10 has the benefits of lower complexity for computing the partial N -IFFT of $\tilde{X}_S[k]$ and less degradation in PAPR reduction performance than that of FIG. 5, where both embodiments determining the index i_s are based on the PAPR level of the data modulated signal \tilde{x}_D .

[0044] Similarly, obtaining the index i during phase optimization can also be based on the PAPR level of the transmitted signal \tilde{x} as illustrated by the embodiment of FIG. 12, thereby further reducing complexity in FIG. 9. FIG. 12 is a more detailed block diagram illustrating the OFDM transmitter shown in FIG. 4, in which the PTS method is used and the parameter control device 200 for PAPR reduction and the side information coding and modulation device 300 are further illustrated. The parameter control device 200 for PAPR reduction comprises a phase optimization unit 60 and a phase mapper 70. The phase mapper 70 is implemented by an encoder 72 followed by an M -ary PSK mapper 71. The PSK sequence $\{b_2, b_3, \dots, b_L\}$ from the M -ary PSK mapper 71 are

imposed onto $(L - 1)$ reserved sub-carriers $\{q_2, q_3, \dots, q_L\}$. The side information coding and modulation device 300 comprises a parity-bit generator 315 for encoding the output from the encoder 72, and a symbol mapper 320 for mapping the parity bits from the parity-bit generator 315 into the sequence $\{d_1, d_2, \dots, d_{\tilde{K}}\}$, and a partial N -IFFT 330 for performing OFDM modulation of the sequence $\{d_1, d_2, \dots, d_{\tilde{K}}\}$ according to the arrangement of sub-carriers $\{p_1, p_2, \dots, p_{\tilde{K}}\}$. This therefore transmits the two-stage coded side information through two groups of reserved sub-carriers $\{p_1, p_2, \dots, p_{\tilde{K}}\}$ and $\{q_2, q_3, \dots, q_L\}$. Similar to the embodiment of FIG. 10, transmission of the index i is completed through the generation of the two sequences $\{d_1, d_2, \dots, d_{\tilde{K}}\}$ and $\{b_2, b_3, \dots, b_L\}$. The corresponding receiver is also similar to that in FIG. 7.

[0045] In the following, some simulation results regarding the embodiments of FIG. 10 and FIG. 12 for verifying the present invention are provided.

[0046] In the simulation, the data $X[k]$'s were assumed to be equally probable 16-QAM symbols with unit variance, and the optimal search PTS method with adjacent partition scheme was used for $L = 4$ (four sub-blocks) and $M = 2$ ($b_l \in \{\pm 1\}$). For the embodiments of FIGs. 10 and 12, the encoder 72 was inexistent due to utilization of optimal search algorithm, and three information bits representing the index i were mapped into the BPSK sequence $\{b_2, b_3, b_4\}$ by the M -ary PSK mapper 71 with the phase mapping as in Table 1. The three information bits were encoded by the linear $(6, 3)$ block code given by (13) where the resultant three parity bits as the outputs of the parity-bit generator 315 were mapped into the BPSK sequence $\{d_1, d_2, d_3\}$ (i.e., $\tilde{K} = 3$) by the

symbol mapper 320. The two sequences $\{d_1, d_2, d_3\}$ and $\{b_2, b_3, b_4\}$ ($\sqrt{P_b} = 1$) were transmitted through two groups of reserved sub-carriers $\{p_1, p_2, p_3\} = \{N/8, 3N/8, 5N/8\}$ and $\{q_2, q_3, q_4\} = \{2N/8, 4N/8, 6N/8\}$, respectively. The PAPR reduction performance was evaluated based on four times oversampling ($R = 4$) of the resultant transmitted signal $\tilde{x}[n]$ (using (3)) and 10^5 Monte Carlo Runs.

[0047] FIGs. 13(a) and 13(b) plot the complementary cumulative distribution function (CCDF), $Pr\{PAPR > PAPR_0\}$, of the obtained 10^5 independent realizations of PAPR levels for 128 and 1024 sub-carriers, respectively. Note that the lines indicated by “ORIGINAL” in these figures are the results obtained without any PAPR reduction, and those indicated by “PTS” are the ones obtained using the optimal search PTS method with no sub-carriers reserving for side information transmission. From FIG. 13(a) ($N = 128$), it can be observed that the embodiment of FIG. 10 exhibits a little bit performance degradation in PAPR reduction whereas that of FIG. 12 slightly improves the PAPR reduction performance. These performance disparities, however, are almost invisible in Fig. 13(b) for the case of $N = 1024$.

[0048] On the other hand, the detection performance for $\{b_2, b_3, b_4\}$ was evaluated by passing the transmitted signal $\tilde{x}[n]$ ($N = 1024$) through a frequency flat channel as well as a two-path frequency selective channel whose impulse response $h[n] = \delta[n] + 0.5\delta[n - 5]$. The received signal was assumed to be corrupted by additive white Gaussian noise (AWGN).

[0049] FIGs. 14(a) and 14(b) plot the word-error-rate (WER) performance of the ‘word’ $\{b_2, b_3, b_4\}$ of the present invention (the lines indicated by “CODED”) for the

frequency flat and frequency selective channels, respectively. For comparison, the results obtained using only the sequence $\{b_2, b_3, b_4\}$ for side information transmission through reserved sub-carriers $\{q_2, q_3, q_4\}$ (i.e., the conventional PTS method) are also plotted (the lines indicated by "UNCODED"). From FIGs. 14(a) and 14(b), it can be seen that the present invention provides about 3 dB and 6 dB coding gains over the conventional PTS method at $WER = 10^{-2}$ for the frequency flat and frequency selective channels, respectively. These results therefore demonstrate the efficacy of the present invention.

[0050] Although the present invention has been described with reference to the preferred embodiments, it should be understood that the invention is not limited to the details described thereof. Various substitutions and modifications have been suggested in the foregoing description, and others will occur to those of ordinary skill in the art. Therefore, all such substitutions and modifications are intended to be embraced within the scope of the invention as defined in the appended claims.